

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Rajesh Khamankar, et al.

Docket No: TI-33223

Serial No:

10/702,234

Conf. No:

7943

Examiner:

William D. Coleman

Art Unit:

2823

Filed:

11/06/2003

For:

RELIABLE HIGH VOLTAGE GATE DIELECTRIC LAYERS USING A DUAL NITRIDATION

PROCESS

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450 MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 8-4-04

Dear Sir:

In the matter of the above identified application, Applicants are submitting herewith copies of documents listed in the attached form PTO/SB/08A for the Examiner's consideration.

In accordance with 37 CFR 1.97 (c), this Information Disclosure Statement is being submitted before the mailing date of a Final Action or a Notice of Allowance and is accompanied by the following certification specified in 37 CFR 1.97 (e).

On information and belief, I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Pursuant to 37 CFR 1.97 (c), Applicants respectfully petition that this Information Disclosure Statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668, and please credit any excess fees to such Deposit Account.

Respectfully submitted,

Peter K. McLarty

Attorney for Applicants

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Substitute for Form 14	49A/PTO		Application Number	10/702,234			
AUG 1 0 2004 INFORMATION DIS	CLOSURE		Filing Date	11/06/2003			
A CHATEMENT DV AL			First Named Inventor	Rajesh Khamankar, et al.			
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(use as many sheets as	necessary)		Examiner Name	William D. Coleman			
Sheet 1	of	1	Attorney Docket No.	TI-33223			

				U.:	S. PATENT DOCU	MENTS	
		U.S. Patent Document		Name of Patentee	Date of Pub.		
Exam. Initials*	Cite No. ¹	Number	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	AA	6,458,663	B1	Moore et al.	10/01/2002		
	AB	6,087,236		Chau et al.	07/11/2000		
	AC						
	AD AE						
AF							
	AG						
	AH						
	Al						
	AJ						

	FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee	Date of Pub.		•	
		Office ³	Number ⁴	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶	
	BA JP 04 154162 A NEC Corp.		NEC Corp.	05/27/1992	(Patent Abstracts of Japan, Vol. 0164, No. 41)				
	BB						•		
	ВС								
	BD								
	BE								
	BF								
	BG								
	ВН								
	ВІ								
	BJ								

	NON PATENT LITERATURE DOCUMENTS					
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Examiner	Date	
Signature	Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.